



3 days Faculty Development Program in collaboration with Texas Instruments



Electronics Engineering Department
Dr. A.I.T.H. Kanpur

Organizes

Faculty Development Program on "Embedded System Design using MSP430"

On

12-14th November, 2016

at

Texas Instruments Center of Excellence,
Electronics Engineering Department
Dr. Ambedkar Institute of Technology for Handicapped
Awadhपुरi, Kanpur - 208024
Phone - 0512-2583221
<http://aith.ac.in>



About the Program

A 3-days faculty development programme on Embedded System Design using MSP430 under Texas Instruments Center of Excellence is being organized by Dr. A.I.T.H. Kanpur. This workshop aimed to provide the state-of-art knowledge in the area of Embedded System to learn real world concepts and complement it with unique hands on experience in Embedded MCU domain. Embedded workshop is based upon low power 16-bit MSP430 series platforms. The workshop will be design to give hands on experience so that every participant will get expertise in using MSP430 platform. From standalone applications to embedded networking applications(embedded Wi-Fi) will be covered with exposure to real world interfacing techniques.

Introduction

Course Objective

- Inculcate and learn application /project oriented teaching methodology in current academic framework
- Understand the system approach for building applications around TI technologies Empower faculties with necessary knowledge, skills and expose them to TI technologies and thereby bridging the gap between industry and academia

Course Content

- Embedded system and role of TI platform: Basic concepts, framework and role of faculty
- Introduction to MSP430series platforms: scope, application and tools in embedded ecosystem

- MSP430's internal architecture and programmer module v Various serial interfaces UART / I2C / SPI with programming
- Embedded of Wi-Fi and Internet of things using CC3100 booster pack v Real time data gathering(humidity, temperature, pressure etc) and remote monitoring for wireless sensor network and related use cases
- Project case studies based upon MSP430 MCU Platform

Speakers

Speakers / Trainers will be from Texas Instruments

Who can Apply

Faculty from AICTE approved colleges / institutes are eligible to participate in this program.

How to Apply

The scanned copy of duly filled application form should be submitted **by email** before **4th November, 2016** and hardcopy of duly filled application form along with **DD in favor of Director, Dr. Ambedkar Institute of Technology for Handicapped Kanpur** payable at Kanpur should be submitted by post on or before **8th November, 2016**.

Registration And Accommodation

Registration fee for faculty/teachers of AICTE approved colleges /institutes are as:

Rs. 1000/- (without accommodation)

Rs. 2000/- (includes accommodation)

Subject to the refund in case A.K.T.U. provides the appropriate fund for the program.

Chief Patron

- Prof. Vinay Kumar Pathak
Vice Chancellor, APJAKTU Lucknow

Patron

- Prof. J. P. Saini
Director, Dr. AITH Kanpur

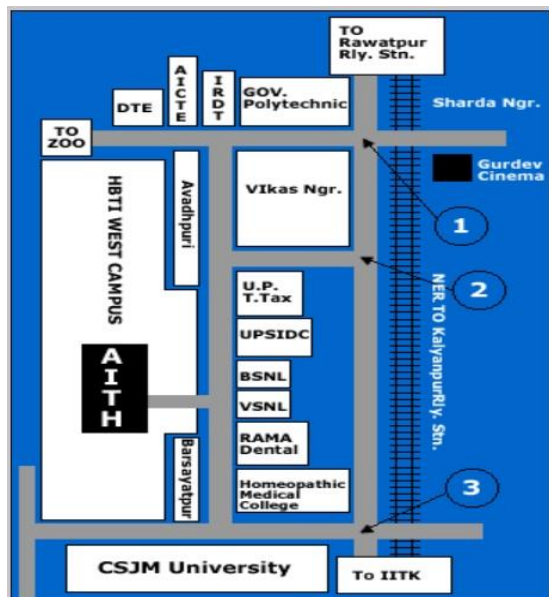
Coordinator

- Dr. K. A. Misra
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How to reach Dr. A.I.T.H.



About Dr. AITH

A.I.T.H is an autonomous institution established in 1997 under department of Technical Education, Govt. of U.P. The Institute is recognized under section 2(f) of the U.G.C. Act 1956 and affiliated to Dr. A.P.J. Abdul Kalam Technical University Lucknow for B.Tech program and Board of Technical Education U.P. (BTE) for Diploma program. All the programs are approved by A.I.C.T.E. New Delhi. Institute is committed to fill the gap between differently able and normal students by providing an environment of effective learning and innovation for developing their skills, broaden their mental horizon and nurturing them into competent and talented professionals to meet the growing challenges of industry and society.

Vision

To develop competent technical manpower in person with different abilities for catering the needs of world of work and to become center of excellence through imparting quality technical education inline with Global Standards



Address for Application Submission

Shweta Tripathi
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Kanpur-208024
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Registration Form

Embedded System Design - Using MSP 430
(12-14 November 2016)

1.Name:

2.Designation:

3.Department:

4.Organization:

5.Address for communication:

Tel:

Mobile:

Email:

Detail of D.D.:

Accommodation required: Yes / No

Signature of the candidate:

Date:

Signature & Seal of
Principle/Registrar/Director

Date: